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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Unerne, Masakatsu

Serial No.: 10/657,464

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Title: Data Processing Apparatus that
Shares a Single Semiconductor Memory
Circuit Among Multiple Data Processing
Circuits

Attorney Docket No.: N26532602E

Group Art Unit: 2185

Examiner: Kim, Hong Chong

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RESPONSE TO FINAL OFFICE ACTION

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Commissioner for Patents
P.O. Box 1450
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10 A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated December 19, 2006. The two month Advisory Action period ends February 19, 2007.

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I hereby certify that this correspondence is being

37 C.F.R. §1.8

transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-571-273-8300
Date of Transmittal: February 16, 2007

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